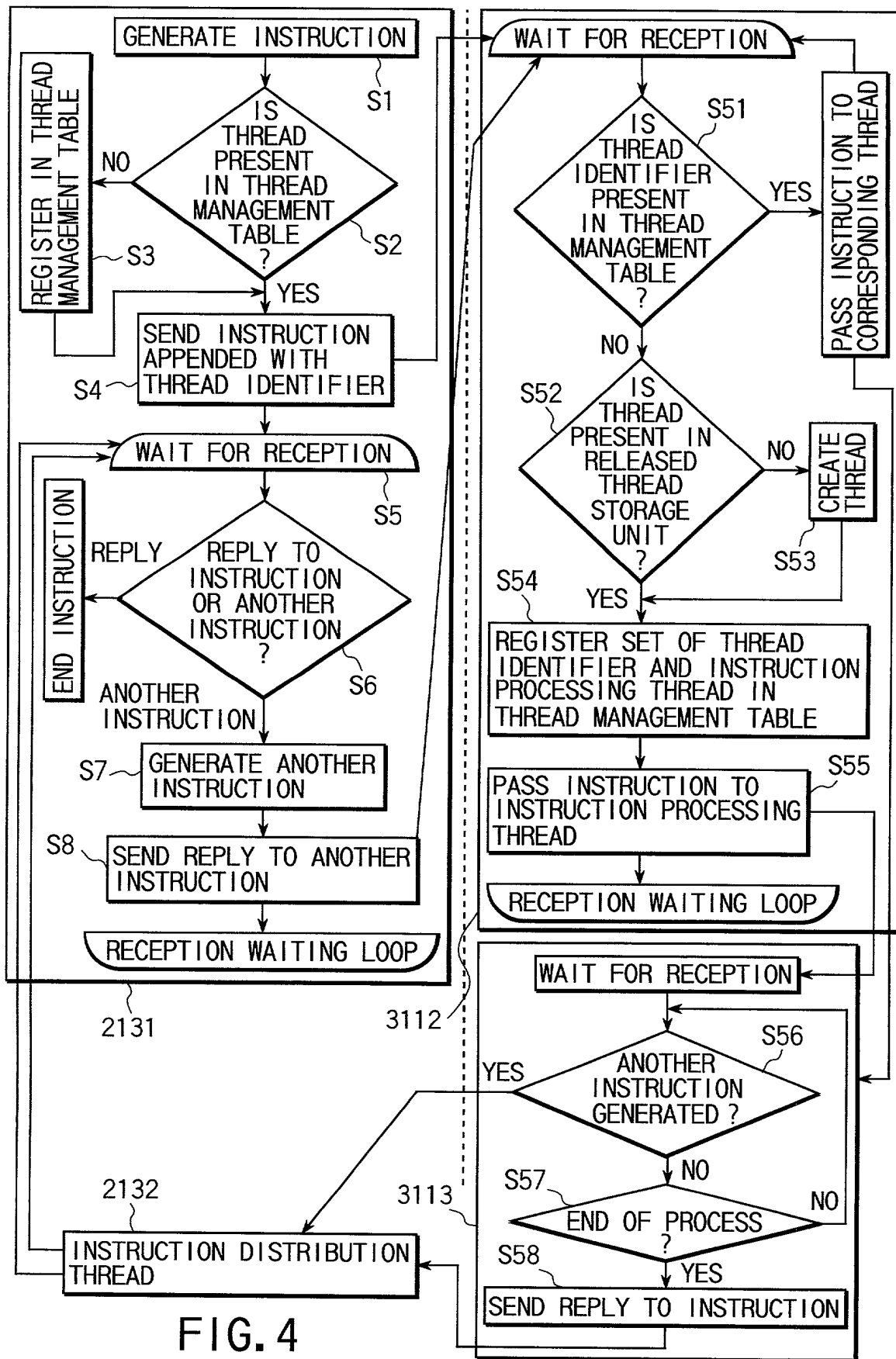


FIG. 1 (PRIOR ART)





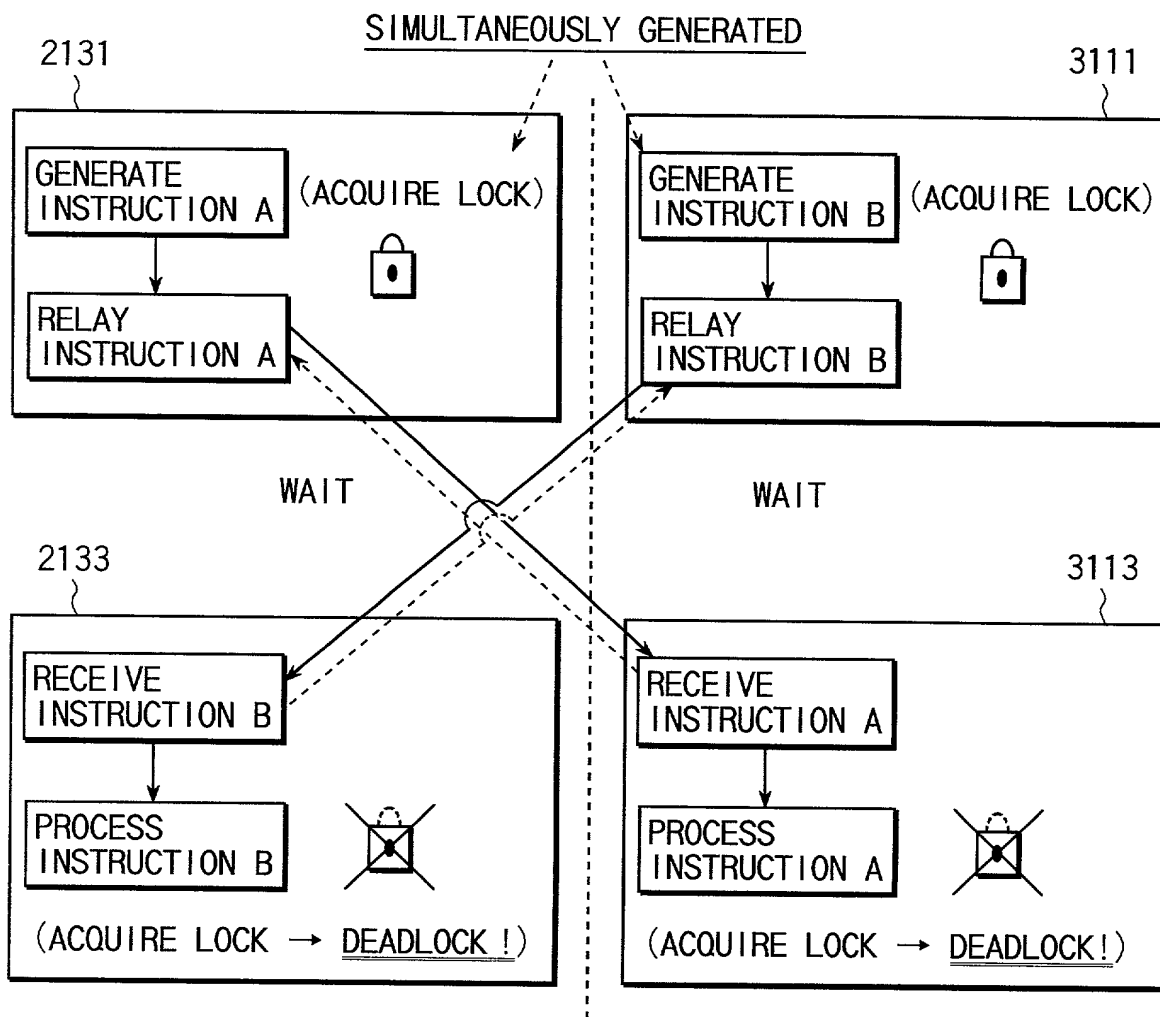


FIG. 5

LOCK #	THREAD #
XXXXX	XXXXX
⋮	⋮

FIG. 7

FIG. 6 is a block diagram of a system architecture showing the interaction between an application, a higher-level library, and an instruction relay library, and how these interact with an instruction execution module, a higher-level library, and a lower-level library through a central interface.

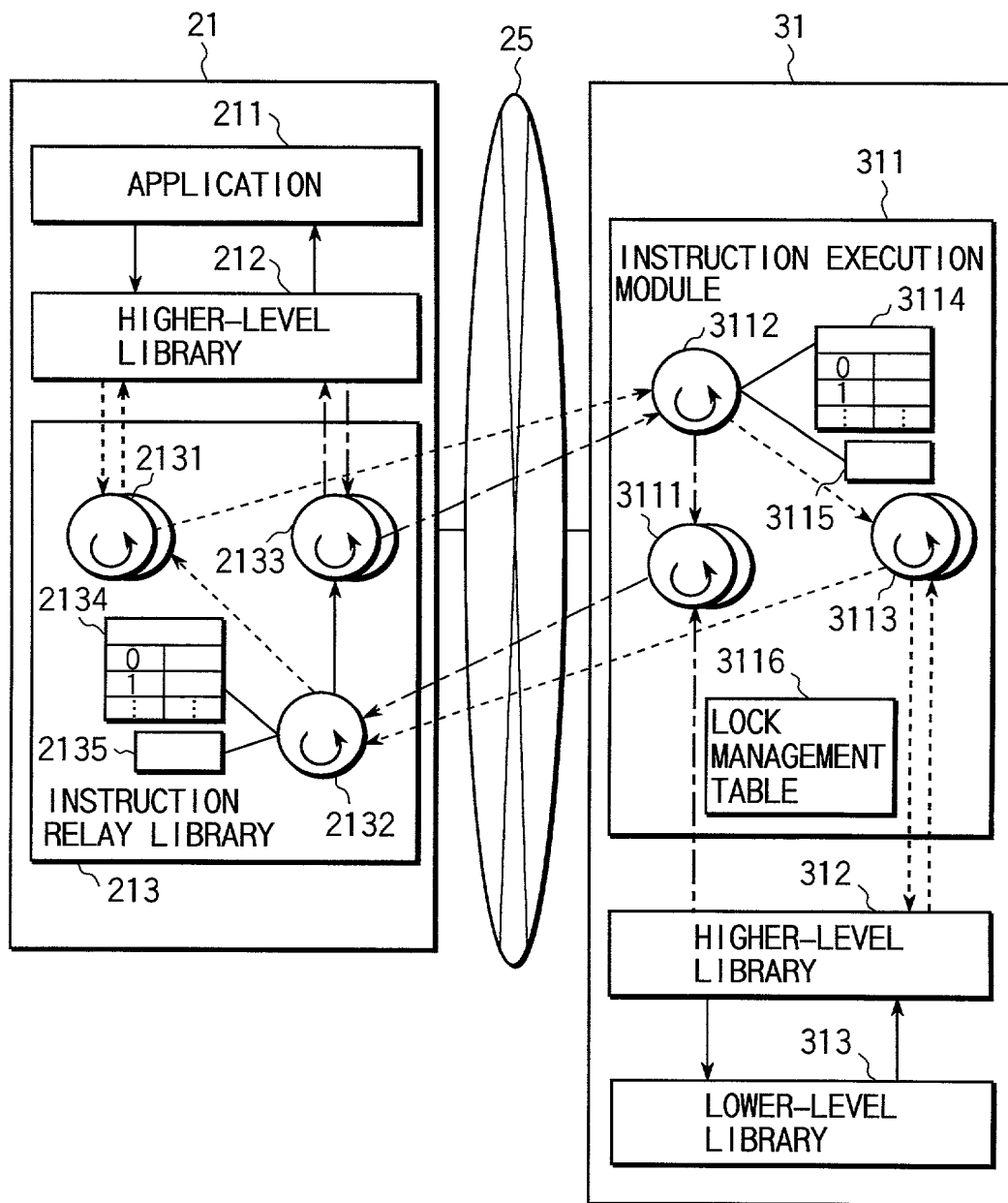


FIG. 6

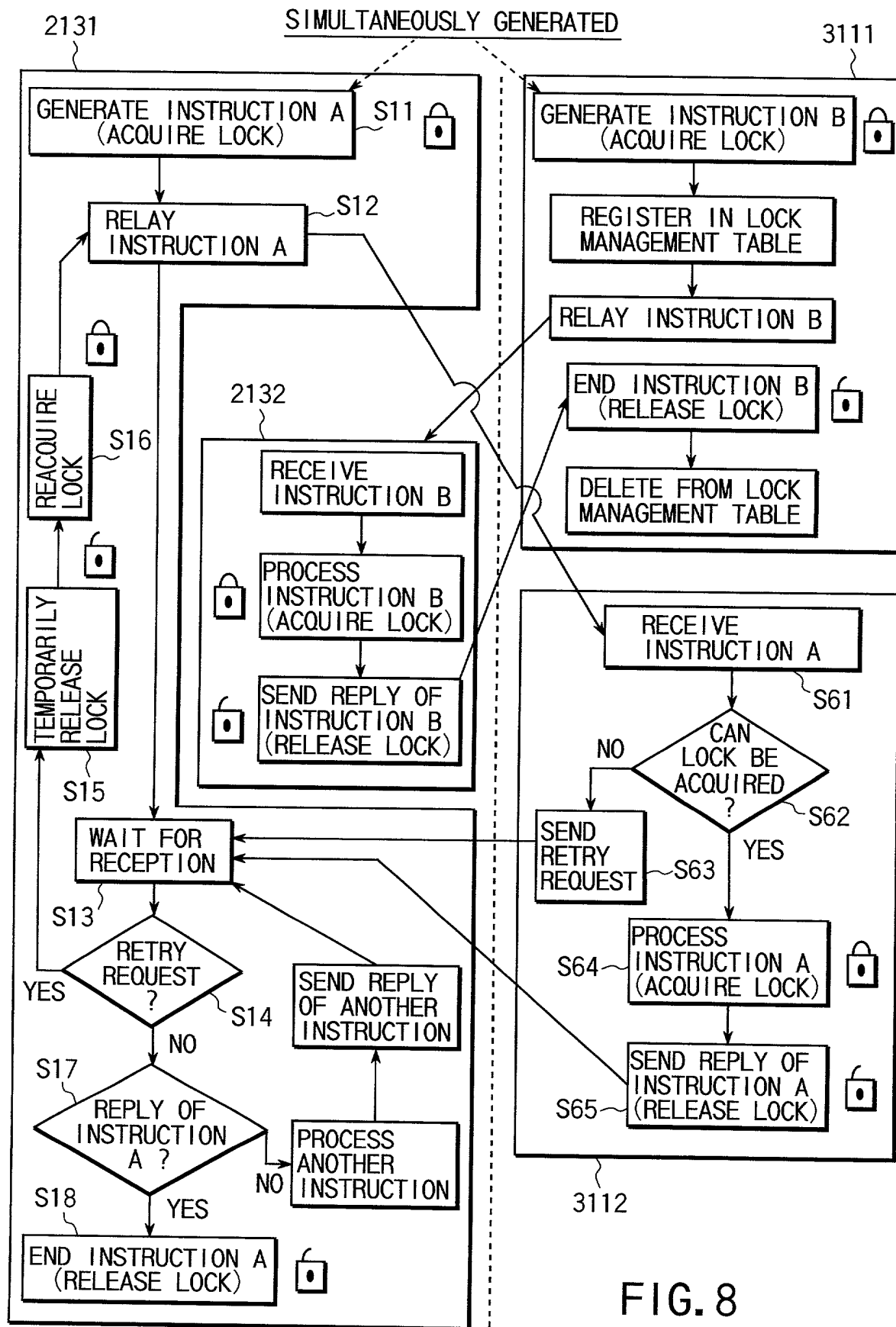


FIG. 8

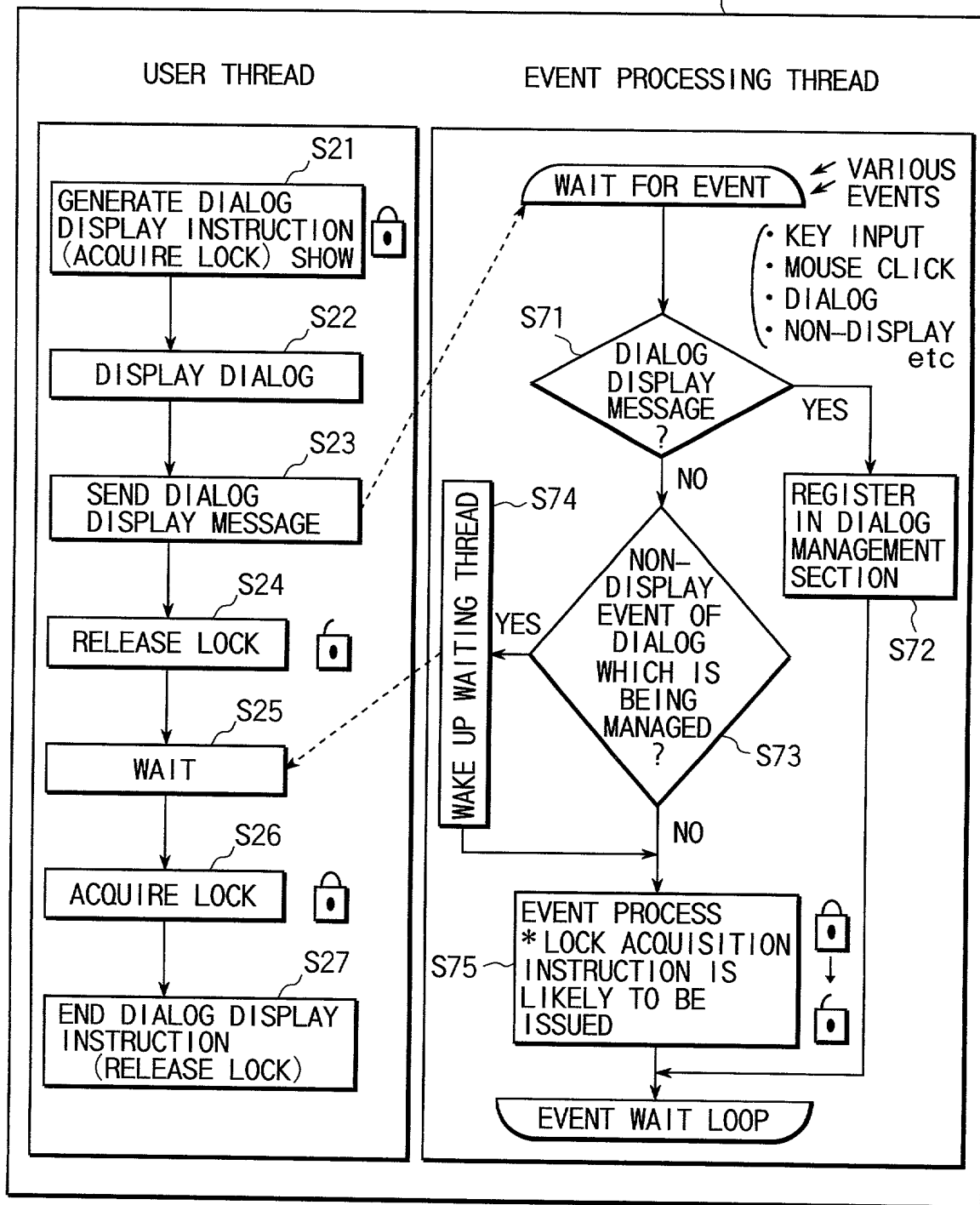


FIG. 9

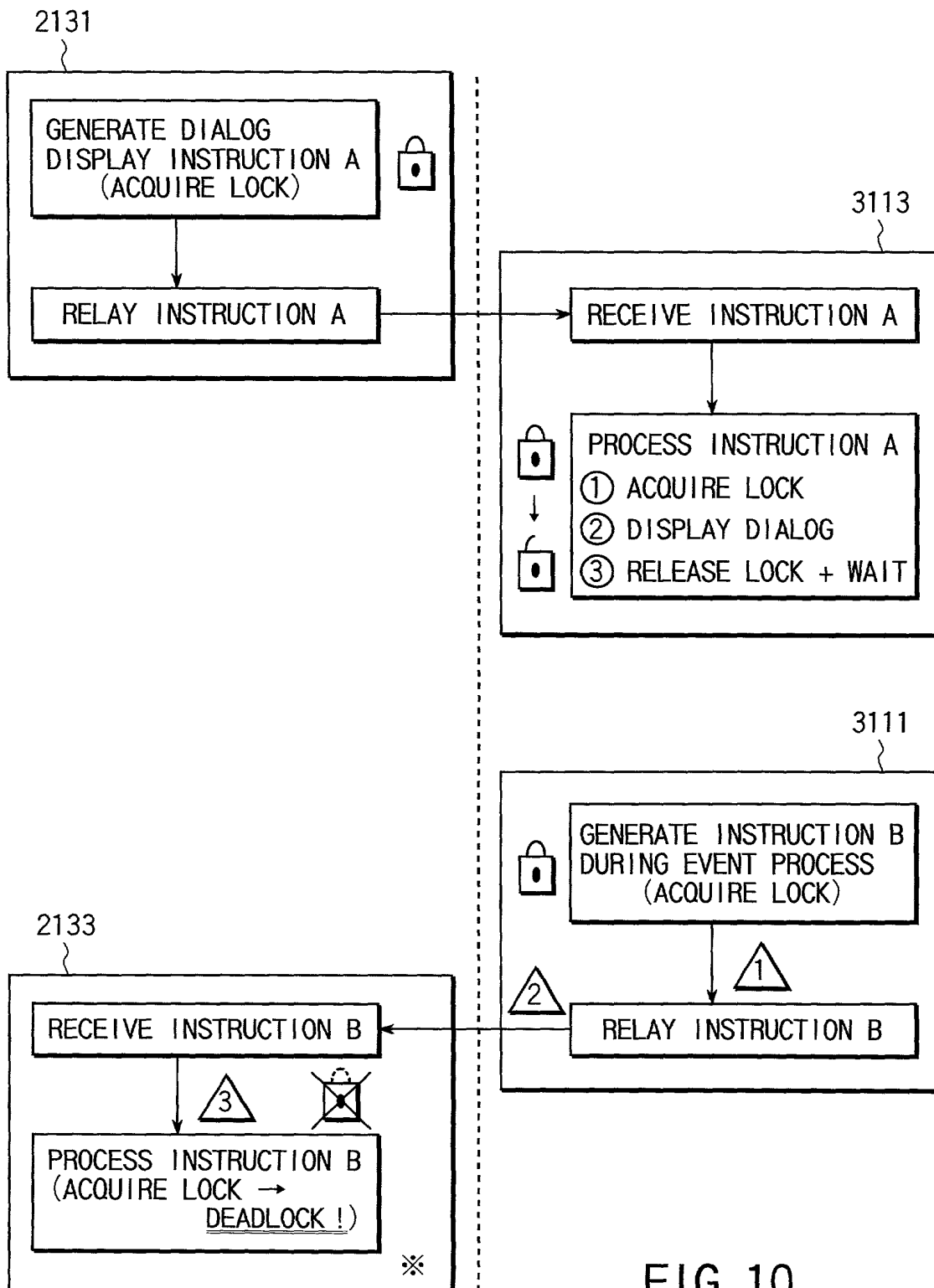


FIG. 10



